

June 14, 2004  
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**CLAIM LISTING:**

A complete listing of the currently pending claims reads as follows:

- 1 (Original) A system for testing electronic modules comprising:  
at least one mapping board box including at least one harness port;  
at least one harness operably connected at one end to the at least one harness  
port,  
the mapping board box including a plurality of pin receptors in electronic  
communication with the at least one harness port, the pin receptors adapted to communicate  
with a circuit printed on a circuit board with pins, wherein the mapping board box is pre-  
wired to receive circuit boards with a variety of pin configurations.
- 2 (Original) The system of claim 1 wherein the dimensions of the at least one  
mapping board box are sized to fit a printed circuit board
3. (Original) The system of claim 1 wherein the at least one harness port  
comprises 56 pins.
4. (Original) The system of claim 1 wherein the at least one mapping board box  
comprises 560 pin receptors.
5. (Original) The system of claim 1 wherein the at least one harness comprises a  
generic harness

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6 (Previously Presented) The system of claim 5 wherein the generic harness comprises 56 wires.

7. (Previously Presented) The system of claim 1 further comprising an electronic simulator.

8. (Original) A method of testing an electronic module, the method comprising pinning a circuit board corresponding to an electronic module; and inserting the pinned circuit board into a pre-wired mapping board box

9 (Original) The method of claim 8, the method further comprising receiving the pinned portion of the board into connection ports in a mapping board box.

10. (Original) The method of claim 9, the method further comprising communicating between a circuit and the module via the mapping board box and a harness connection.

11. (Original) A system for testing an electronic module comprising:  
means for receiving a circuit board;  
means for communicating between the circuit board receiving means and an electronic module.

12 (New) The method of claim 8 wherein the mapping board box includes a plurality of pin receptors in electronic communication with the at least one harness port, the pin receptors adapted to communicate with a circuit printed on a circuit board with pins.